AMENDMENTS TO THE SPECIFICATION

Please replace the paragraph at page 7, lines 3-15, with the following rewritten paragraph:

In a switch with distributed architecture, such as the one of Figure 1b, this translation can be performed by the ATM layer function of the ingress junctor. The cell is then sent back to the cross-connector module together with an indication of the egress cross-connection junction to which the cross-connector must switch the cell. This indication can be ~~despatched~~ dispatched in a specific header prefixed to the start of the cell. Translation devices in accordance with this particular case have been described by the applicant, for example in French patent applications No. ~~2 670 972, 2 681 164, 2 726 669~~ 2,670,972, 2,681,164, 2,726,669 and French application FR 97 07355 not yet published.

Please replace the paragraph at page 7, line 26, continuing to page 8, line 4, with the following rewritten paragraph:

As regards the conveying of the cells, the model of the point-to-point connection, that is to say the ingress translation only, cannot always be applied. In Figure 3a where the elements counterpart to those of Figure 1b are ~~labelled~~ labeled with the same references, a point-to-multipoint connection is represented. This connection enters the switch via a port P1 and leaves it via the ports P2, P4, P5, P7. In this case, the ingress translation envisaged above can order the cross-connector 5 to copy each cell of this connection to the three cross-connector junctions concerned (7_3, 7_4, 7n) but it is not capable of indicating the egress ports to which the cell should be ~~despatched~~ dispatched. To do this, it is necessary to append this information to the translation tables and to convey it from the ingress to the egress. Moreover, the egress logical path depends on each egress port and it cannot be allocated a unique logical path for all the egresses.